



PATENT
2332-0117P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: KIM, Jeong Soo, et al. Conf. No.: 4564
Appl. No.: 09/998,134 Group: 2823
Filed: December 3, 2001 Examiner: Thanh V. PHAM
For: METHOD OF FORMING A JUNCTION IN SEMICONDUCTOR
DEVICE USING HALO IMPLANT PROCESSING

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

October 21, 2003

Sir:

In response to the Examiner's Office Action dated April 22, 2003, the period for response having been extended three (3) months to expire on October 22, 2003, please amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.

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